

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q62494

Ryo KUBOTO, et al.

Appln. No.: 09/817,233

Group Art Unit: 2823

Confirmation No.: 8072

Examiner: Hsien Ming LEE

TECHNOLOGY CENTER 2800

Filed: March 27, 2001

For:

MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE HAVING DRAM

CAPACITORS

RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated January 15, 2003, please consider the following:

REMARKS

Claims 1-7, 9 and 12-21 are all the claims pending in the application.

As a formal matter, the Examiner has not considered the references submitted with the IDS filed December 4, 2002, allegedly because the IDS does not include a concise explanation of relevance. In this regard, Applicant respectfully submits that, as stated in the IDS, in compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents Applicant submitted a copy of a Communication from the Japanese Patent Office in a counterpart application citing these documents, together with an English-language version of that pertinent portion indicating the degree of relevance found by the Japanese Patent Office. Thus, Applicant has fully complied with the concise explanation requirement of 37 C.F.R. § 1.98(a)(3)